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### MJD44H11 NPN Epitaxial Silicon Transistor

#### **Features**

- General-Purpose Power and Switching such as Output or Driver Stages in Applications
- D-PAK for Surface-Mount Applications
- Lead-Formed for Surface Mount Application (No Suffix)
- · Fast Switching Speeds
- Low Collector Emitter Saturation Voltage



1.Base 2.Collector 3.Emitter

### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
MJD44H11TF	MJD44H11	TO-252 3L (DPAK)	Tape an <mark>d R</mark> eel
MJD44H11TM	MJD44H11	TO-252 3L (DPAK)	Tape and Reel

#### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at T<sub>A</sub> = 25°C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	80	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
I <sub>C</sub>	Collector Current (DC)	8	Α
I <sub>CP</sub>	Collector Current (Pulse)	16	Α
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature Range	- 65 to +150	°C

#### **Thermal Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter		Max.	Unit	
P <sub>D</sub>	Total Device Dissipation	T <sub>C</sub> = 25°C	20	W	
		T <sub>A</sub> = 25°C	1.75	VV	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		6.25	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		71.4	°C/W	

#### **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>CEO</sub> (sus)	Collector-Emitter Sustaining Voltage <sup>(1)</sup>	$I_C = 30 \text{ mA}, I_B = 0$	80			V
I <sub>CEO</sub>	Collector Cut-Off Current	$V_{CE} = 80 \text{ V}, I_{B} = 0$			10	μΑ
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = 5 \text{ V}, I_{C} = 0$	A		50	μΑ
h <sub>FE</sub>	DC Current Gain <sup>(1)</sup>	$V_{CE} = 1 \text{ V}, I_{C} = 2 \text{ A}$	60			
		$V_{CE} = 1 \text{ V}, I_{C} = 4 \text{ A}$	40	1		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage <sup>(1)</sup>	$I_C = 8 \text{ A}, I_B = 0.4 \text{ A}$			1	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage <sup>(1)</sup>	$I_C = 8 \text{ A}, I_B = 0.8 \text{ A}$			1.5	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = 10 \text{ V}, I_{C} = 0.5 \text{ A}$		50	TO	MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = 10 V, f = 1 MHz	7	130		pF
t <sub>ON</sub>	Turn-On Time		400	300		ns
t <sub>STG</sub>	Storage Time	$I_{C} = 5 \text{ A},$ $I_{B1} = -I_{B2} = 0.5 \text{ A}$		500	1	ns
t <sub>F</sub>	Fall Time	.BI .BZ SIG I		140		ns

#### Note:

1. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2%.



### **Typical Performance Characteristics**

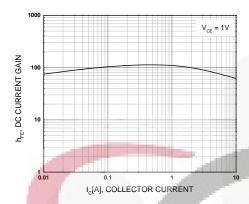


Figure 1. DC Current Gain

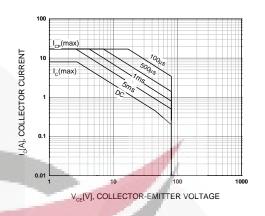


Figure 2. Safe Operating Area

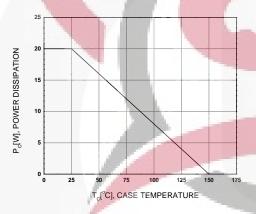
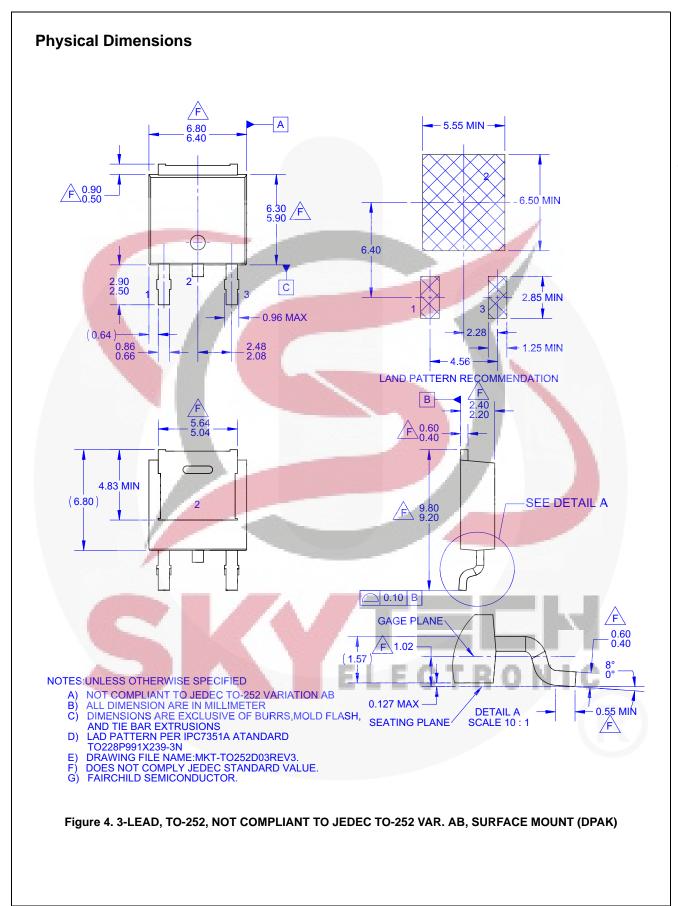


Figure 3. Power Derating









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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.	
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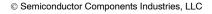
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